ImageWriter

INTEL 25Fxxx Family Device Support Detail & Setup

ImageWriter Support Hierarchy

Manufacturer: Intel **Library:** SPI-Serial_Memory.cab

Device: 25F320S33B, 25F320S33T, 25F160S33B, 25F160S33T, 25F016S33B, 25F016S33T, 25F640S33B, 25F640S33T

Objects for 25Fxxx Family

Flow Object Name:	*Program / Execute*	*Verify*	*Load*	*Object Size (Bytes)*	
FLASH[1]	•	•	•	25F320S33x	0x400000
				25F160S33x	0x200000
				25F016S33x	0x200000
				25F640S33x	0x800000
CHIP ERASE	•			25F320S33x	0x400000
				25F160S33x	0x200000
				25F016S33x	0x200000
				25F640S33x	0x800000
SECTOR ERASE	•			25F320S33x	0x400000
				25F160S33x	0x200000
				25F016S33x	0x200000
				25F640S33x	0x800000
ERASE BLOCK	•			0X10000	
OTP	•	•	•	0x200	
DEVICE ID			•	0x3	
Status Register			•	0x01	
p_SPI[1]	•		•	0x8	

[1] Paged based programming.

[2] Default SPI clock frequency = 8 MHz

Physical Layer Connector Setup for 25Fxxx Family

ImageWriter:		Target:
Connector Pin #	***Pin Name***	***Connections***
1	GND	GND
2	GND	GND
3	TXD (MOSI/TDO)	D
4	RXD (MISO/TDI)	Q
5	RTS (TMS)	NOCONN
6	CTS	NOCONN
7	XCLK	C
8	RST	!S
9	SCL	NOCONN
10	SDA	NOCONN
11	GND	GND
12	GND	GND
13	VPP/EXT_IN	NOCONN
14	VPP/EXT_OUT	NOCONN
15*	Target V+	Supply 2.7 - 3.6V
16*	Target V+	Supply 2.7 - 3.6V
17	GND	GND
18	GND	GND
19	Pod Power	Supply 9-24 VDC
20	Pod Power	Supply 9-24 VDC

Support Notes:

1. A post-programming Flash Verify operation is highly recommended to ensure data integrity.
2. Erase & Program operations are dependent on Block Protect Bit settings in the Status Register. See manufacturer documentation for more details.
3. Sector ERASE will erase the addressed sector (See note 2.)
4. ERASE BLOCK will only function if address is within the Parameter block. See manufacturer documentation for more details.
5. OTP object size is not adjustable; therefore the image file to be programmed must match the entire OTP memory map in size. Attempting to program 0xFF to any location will not cause a data change. (See manufacturer documentation for more details.) The INTEL reserved area can not be programmed, **can** be loaded, and will not effect verify in any way.
6. !W and !HOLD must be tied to VCC.

Disclaimer

Typical implementation of **ImageWriter** includes but is not limited to external power provision to **ImageWriter**, device access restrictions on the target board, external hardware on the target board, or software changes created by the user affecting timing, device erasure, programming, or verification. Data I/O warranty coverage applies only when the product is used in conformance with technical specifications put forth by Data I/O and by the semiconductor device manufacturer.

In the interest of achieving the highest levels of safety and quality, Data I/O recommends that all programming processes be tested and documented in a prototype project prior to transition to volume manufacturing. For additional information see In-System Programming Design Guidelines (http://www.dataio.com/isp).

ImageWriter programs semiconductor devices through the use of circuits designed precisely to operate in accordance with specifications outlined by the device manufacturer. Provision and calibration of power supplies are critical for proper operation. **ImageWriter** has been fully tested by Data I/O Corporation for quality performance.